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the output voltage at the receiver circuit is monitored to show the output voltage and frequency at the end of each experiment. the simulation code is written in xilinx isim software as shown in fig 4. the simulation is set in a full-wave rectifier topology with two stages which are rectifying the input voltage at 315 mhz. this design is used to simulate the operation of the transmitter and receiver circuit. the simulated current from the power management circuit is also monitored. the waveforms and parameter values are taken from the test results obtained from the aloe vera plants experiment. the simulated output voltage is compared with the measured one. the voltage and current from the power management circuit is also monitored. the input voltage of the proposed dc-dc boost converter is designed to be 3.48 v. from fig 9(b), it is seen that when vgs-sw is set to 3.40 v, the peak voltage is at 9.44 v with an average voltage of 3. therefore, the input energy is increasing with every cycle of charging and discharging process of cin. this will cause the voltage at the output to increase accordingly with every cycle of charging and discharging process of cout as well. at the same time, the average of the output voltage is increasing with every cycle of charging and discharging process of cout. the period of charging and discharging process is defined as the duty cycle, d, which is defined in eq (1). this is shown in fig 10, where the average of the output voltage is calculated from eq (2). the energy harvested from the aloe vera plants is transferred to the output capacitor, cout in a series of cyclic charging and discharging process and stored in the capacitor, cin as shown in fig 9(a). the average of the output voltage is increasing with every cycle of charging and discharging process and it is observed that the output voltage at 10.9 v is maintained for a long period of time. this means that the energy harvested from the aloe vera plants is able to be stored in cin and is able to be transferred to the output capacitor, cout when the wireless transmitter load is activated. this means that the proposed self-oscillating boost converter is able to boost the input voltage from 3.48 v to 10.9 v.

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